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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.	
Filing Date	June 11, 1999
Inventor K	
Assignee	
Group Art Unit	
Examiner	
Attorney's Docket No.	MI22-532
Title: "Methods for Forming Wordlines, Transistor	r Gates, and Conductive
Interconnects, and Wordline, Transistor Gate, an	d Conductive Interconnect
Structures"	

## **DECLARATION OF MICHAEL L. LYNCH**

- I, MICHAEL L. LYNCH, hereby declare as follows:
- 1. I am employed as Chief Patent Counsel at Micron Technology, Inc., a Delaware corporation located in Boise, Idaho.
- 2. My address is at Micron Technology, Inc., 8000 S. Federal Way, Boise, ID 83706-9632.
- 3. Micron Technology, Inc. is the assignee of the entire right, title and interest of the above-identified application as evidenced by the accompanying "Assignment of Inventions, Rights and Confidential Information Agreement" which was executed by the inventor Mr. Klaus Florian Schuegraf on June 8, 1994, a copy of which is attached hereto as Exhibit "A", and as evidenced by the accompanying "Assignment" executed by the other inventor, Mr. Randhir P.S. Thakur. I have reviewed the Declarations, "Assignment" and "Assignment of Inventions, Rights and Confidential Information Agreement", and certify that, to the best of my knowledge and belief, title is in Micron T chnology, Inc. I

am empowered to sign this statement on behalf of Micron Technology, Inc., the assignee of the application.

- 4. Upon information and belief, Mr. Klaus Florian Schuegraf, last of 26895 Aliso Creek Rd., Aliso Viejo, CA 92656, and a citizen of the United States of America, is an original and joint inventor of the above-identified invention entitled "Methods for Forming Wordlines, Transistor Gates, and Conductive Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect Structures", and was an employee of Micron Technology at the time of conception of the invention.
- 5. I have reviewed and understand the contents of the aboveidentified specification, including the claims.
- 6. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, \$1.56(a).
- 7. Upon information and belief, the filing of the above-identified application is necessary to preserve the rights of Micron Technology, Inc. in the subject invention as a "Notice to File Missing Parts of Application" has been issued in this matter.

\* \* \* \*

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and, further, that these statements were made with knowledge that willful false statements and the like so made

are punishable by fine or imprisonment, or both, under §1001 of Titl 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

MICHAEL L. LYNCH

Reg. No. 30,871

# MICRON SEMICONDUCTOR, INC.

# Confidential information agreement Assignment of inventions, rights, and

in retailereden of my emphyment by MICEON SEMICONDUCTOR, INC. or my of its sittlesed comparies (herebastes, collectively referred to us the emphyme), I hereby agree as fallows:

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This agreement has been read, write stood, and it agreed on

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